notifying us and returning the facsimile to us.

ATTORNEYS & COUNSELORS

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FACSIMILE TOTAL PAGE COUNT: DATE: March 17, 2004 **DIRECT DIAL NUMBER:** (972) 744-2902 FROM: Robert C. Klinger -PHONE NUMBER FACSIMILE NUMBER **COMPANY NAME** NAME 703-872-9306 Examiner Lefkowitz USPTO This facsimile is intended only for the use of the PLEASE CALL (972) 744-2900 addressee. If the addressee of this facsimile is a client or IF YOU DO NOT RECEIVE ALL PAGES. agent for one of our clients, you are further advised that the facsimile contains legally privileged and confidential information which we intended to send to the addressee only. FOR INTERNAL USE ONLY: In any event, if you are not the intended recipient of the facsimile, you are hereby notified that you have received this facsimile inadvertently and in error. Any review, dissemination, distribution or copying of this is strictly prohibited. If you have received this in error, please immediately notify us by telephone and return the original CLIENT/MATTER NO.: 101950.00027 facsimile to us at the address above via the United States Postal Service. We will reimburse any costs you incur in RCK1

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Frank W. Ahern

Art Unit:

2181

Serial No.:

091993,34709/819053

Examiner:

Lefkowitz, Sumati

Filed:

Mary 20, 2000

Docket:

101950.000

OFFICIAL

SYSTEM ENABLING DEVICE COMMUNICATION IN AN EXPANDED For:

COMPUTING DEVICE

PRELIMINARY AMENDMENT

March 17, 2004

Commissioner For Patents Alexandria, VA 22313-1450

FACSIMILE CERTIFICATE UNDER 37 C.F.R., 1.8(A)
I hereby certify that the above correspondence is being sent via Pacsimile

Sir:

Prior to the examination please amend the above identified application as follows:

In the Claims

Please amend Claims 1-5, 7-8, and delete Claims 6, and 9-23 as follows:

- 1. A system for enabling device communication in an expanded computing device comprising:
- a primary bus coupled to a first integrated circuit ehip having a first register and a second register therein;
- a secondary bus physically remote from the primary bus coupled to a second integrated circuit chip having a third register and a fourth register therein;
 - a first serial link coupled between the first register and the third register; and
 - a second serial link coupled between the second register and the fourth register-; and

wherein the first integrated circuit is configured to enable the transfer of data to the